

Training course: NXP MC33PT2000/1

Training course agenda proposal:
(depending on customer's needs it might be adapted)

- **DAY 1:**
 - **Morning: Overview on MC33PT2000/1 family and general features**
 - **Afternoon: Introduction to MC33PT2000/1 family: Analog Modules, Digital Core**
- **DAY 2:**
 - **Morning: Register description and auxiliary functions**
 - **Afternoon: Basic instruction set description and microcode functional example**
- **DAY 3:**
 - **Morning: Advanced instruction set**
 - **Afternoon: Advanced microcode, handling diagnosis**
- **DAY 4:**
 - **Open discussion and specification for exercise (due for training Day 5)**
- **DAY 5:**
 - **Morning: Analysis of the due exercise (from training Day 4)**
 - **Afternoon: Optimal solution of the due exercise (from training Day 4)**
- **DAY 6:**
 - **Morning: Analysis of the specification of a sample application microcode**
 - **Afternoon: Lab test and demo of sample microcode (functional part), includes tutorial on NXP SPIGen**
- **DAY 7:**
 - **Morning: Lab test and demo of sample microcode (diagnosis), includes tutorial on Tracing**